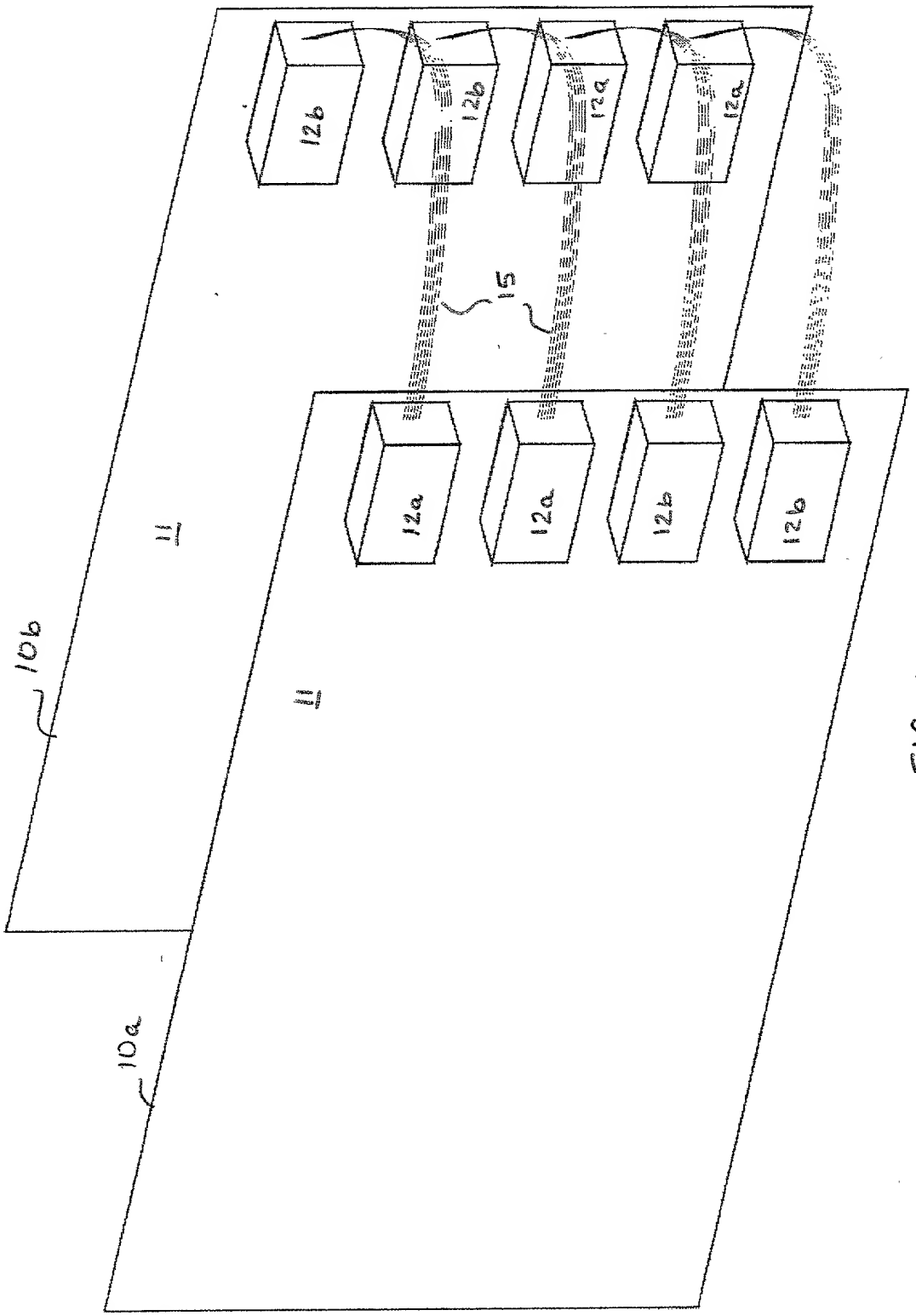

FIG. 1

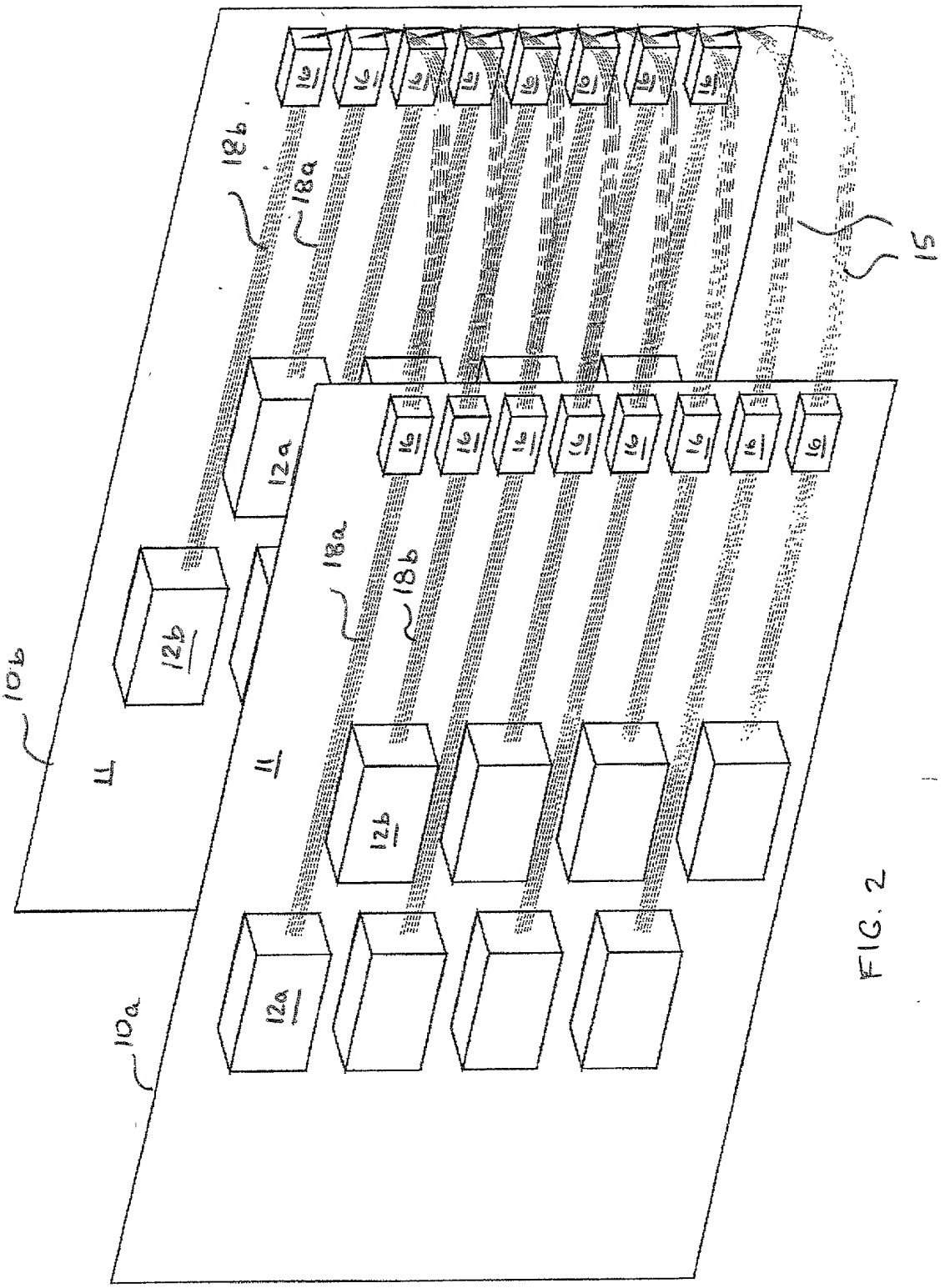

FIG. 2

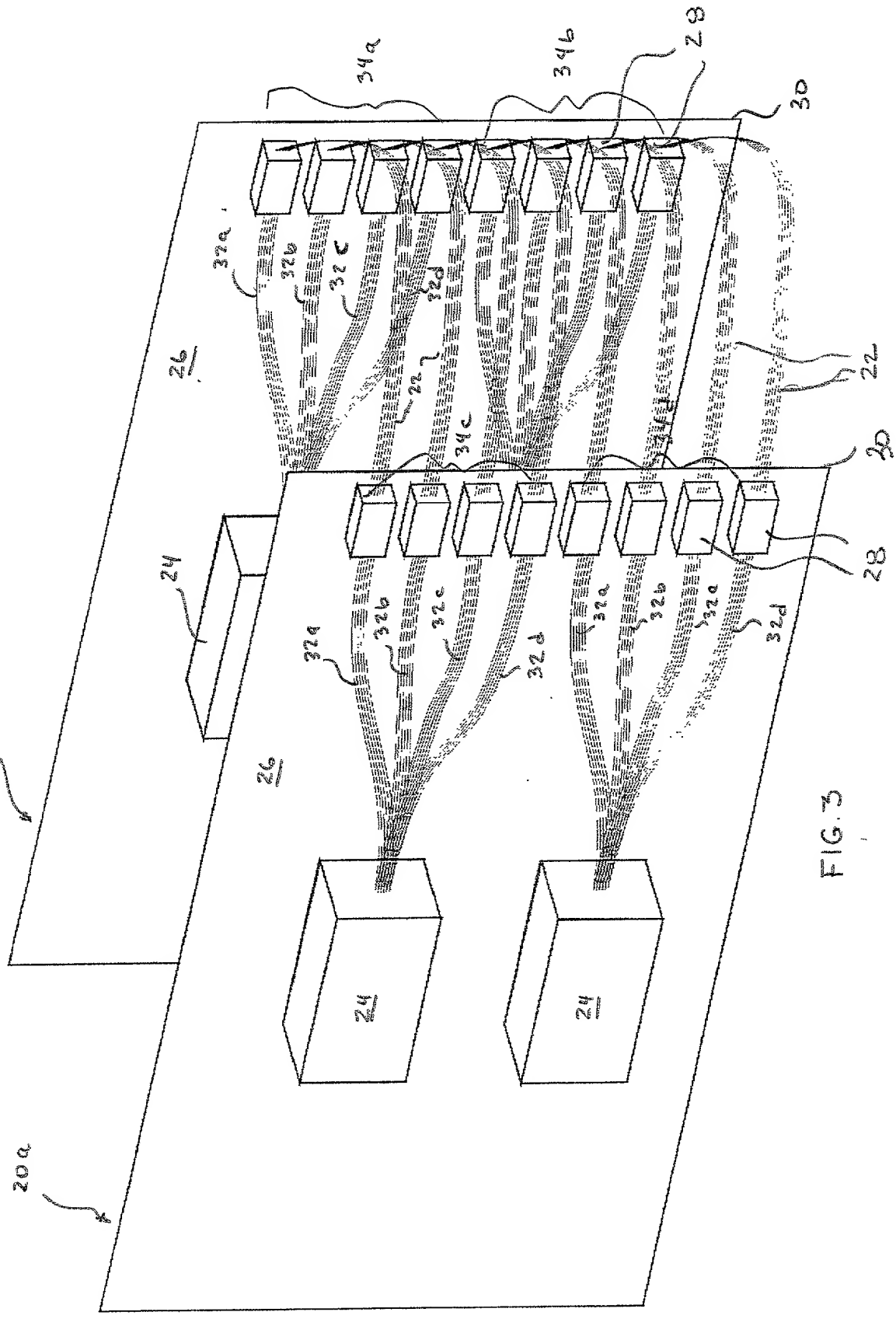

FIG. 3

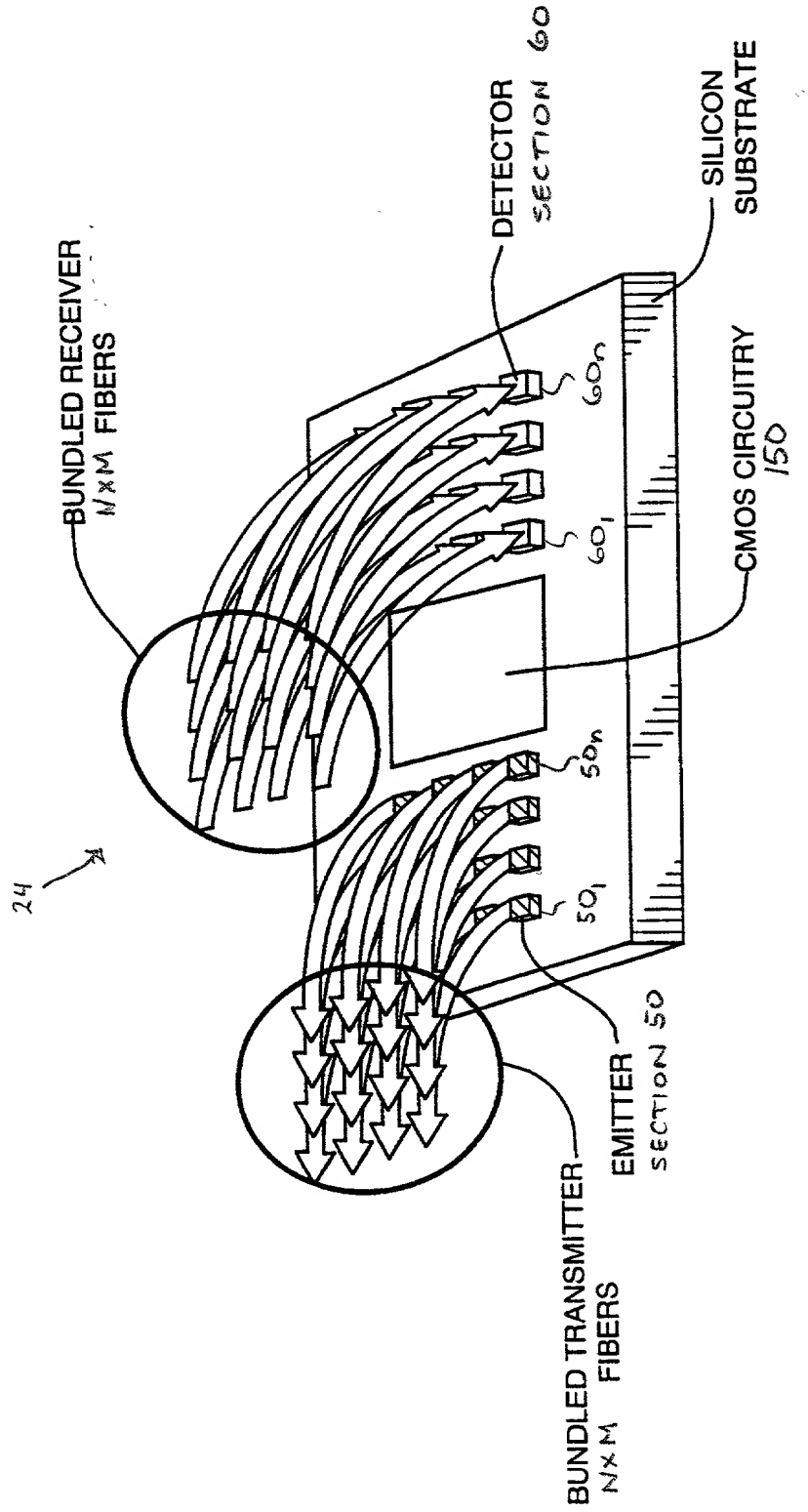
BUNDLED RECEIVER
N x M FIBERS
DETECTOR
SECTION 60
SILICON
SUBSTRATE
CMOS CIRCUITRY
150
60n
60₁
50n
50₁
24
BUNDLED TRANSMITTER
N x M FIBERS
EMITTER
SECTION 50

FIG. 4

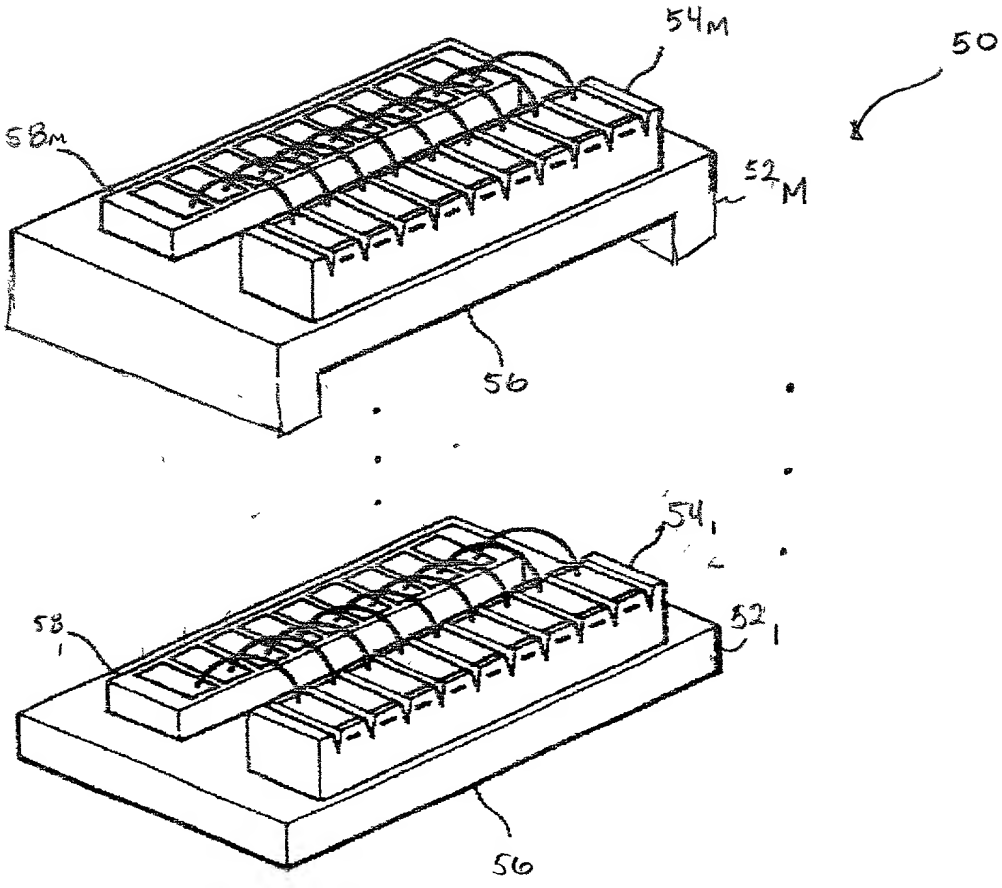

FIG. 5A

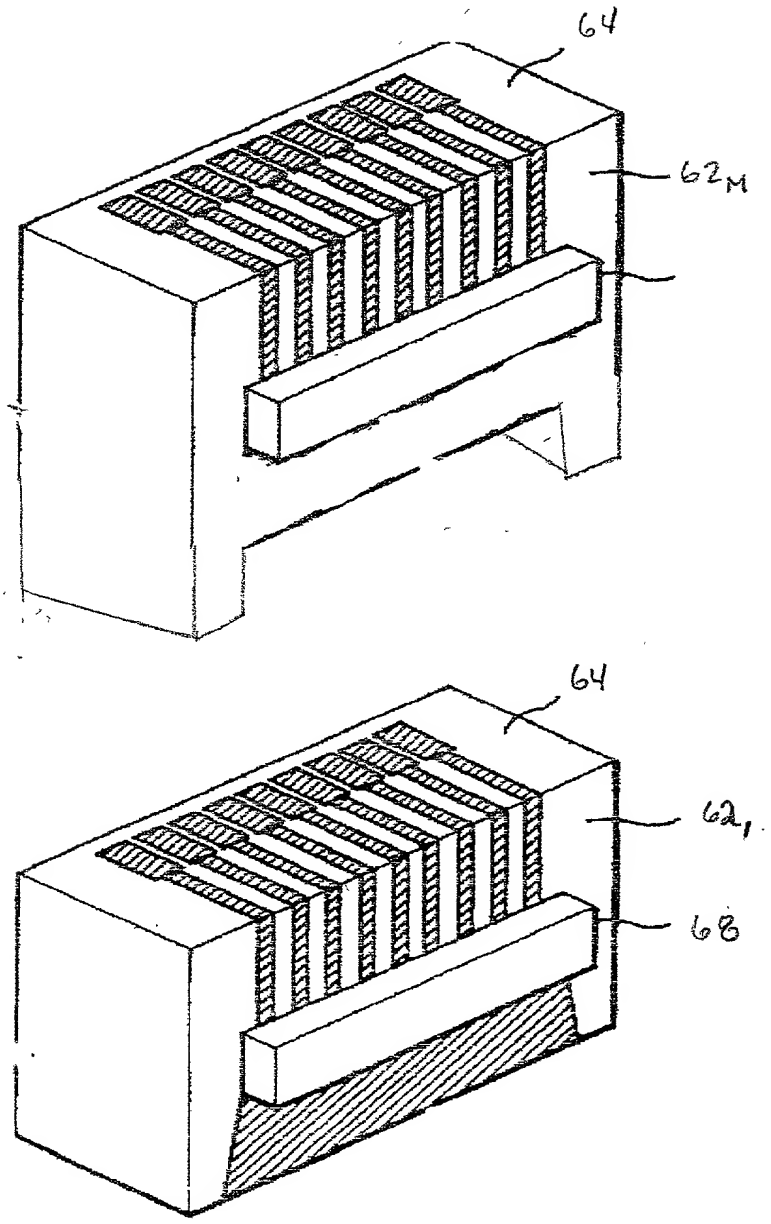

FIG. 5B

EP 0 995 211 B1

FOR090 F4924860

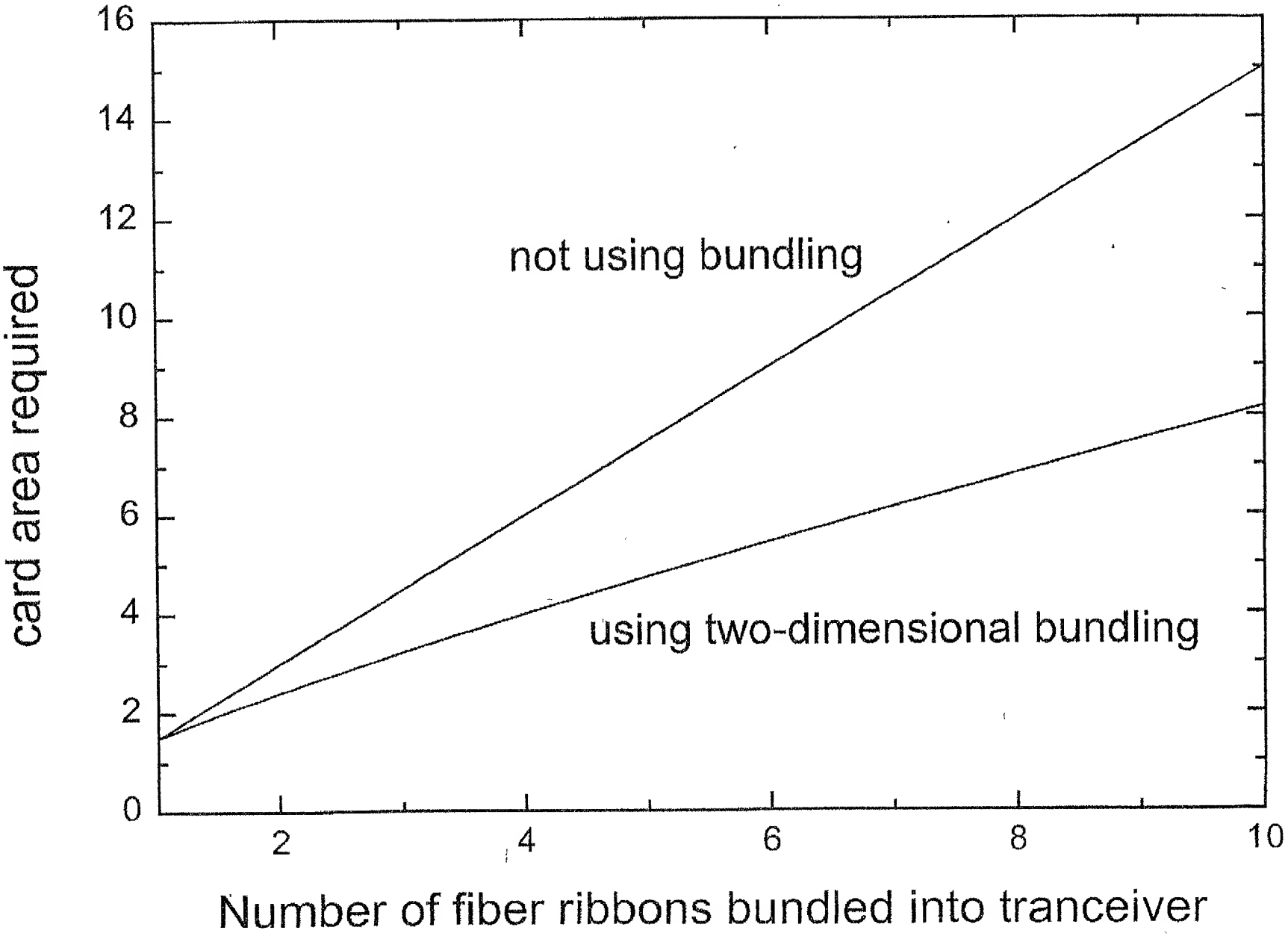

FIG. 6